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5/19/10 Expedited procedure
Under 37 CFR 1.116

1204.46401X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. TANAKA, et al.

Application No.: 10/586,305

Filed: JULY 14, 2006

For: MANUFACTURING METHOD OF A SEMICONDUCTOR
DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN
IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT
LAYERS ELECTRICALLY CONNECTED TO THE
ELECTRODES

Group: 2829

Examiner: Shaun M. Campbell

Confirm. No.: 2516

AMENDMENT AFTER FINAL REJECTION

Mail Stop: AF - FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

May 11, 2010

SIR:

In response to the Office Action mailed January 11, 2010, the period for response having been extended for one (1) month by the attached Petition for Extension of Time, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.